

# ENDRIASE YETBARK

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## OBJECTIVE

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A recent graduate in Engineering Physics(2021), seeking for full-time job in the tech industry, with specific interests in Software or Analog/ digital electronics related fields.

## EDUCATION

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**Bachelor in Engineering Physics**, Carleton University

Graduated: Nov-2021

Relevant Coursework: Digital/Analog Electronics, Object Oriented Programming, Nanotechnology and Fiber Optics

## SKILLS

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**Technical Skills**

Analog Circuit Design, Circuit Analysis, Soldering, Oscilloscopes and Multimeters

**Programming and other skills**

Python, MATLAB, C++, C, JAVA, Verilog, P-Spice, LT-Spice

**Language**

Amharic (Professional), English(Professional)

## MAJOR PROJECTS

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**Photonic Temperature Sensor**

Sept 2020 - May 2021

Carleton University

Ottawa, ON

- Designed a photonic temperature sensor for harsh environments using a Fiber Optic cable.
- Used software tools such as Optigrating and COMSOL to perform simulations of the equipment.
- Specifically worked on designing the Optical component and signal processing system.

**Verilog based Cook Timer**

March 2020 - Apr 2020

Carleton University

Ottawa, ON

- Designed a cooking timer using a Verilog code that uses various modules to implement a basic egg timer on a NEXUS-4DDR FPGA board.
- The timer included various modules to count down, set the alarm and display the final message.
- An additional flashing signal is displayed in case if the user doesn't detect the first signal.

**Online Library**

Feb 2022

[Personal Project](#)

Ottawa, ON

- Built an online library using FLASK and SQL-Alchemy in which users can add, remove or even get information about the books currently stored in their library.
- The Books are stored in an SQL Database server and are retrieved by their ISBN number while other information(such as title, published date, etc) is given as an additional supporting input.

## OTHER PROJECTS

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**BandPass Filter Project:** Designed, simulated, and built an op-amp based filter circuit on a breadboard that would generate a Chebyshev response

**Count Down/Up timer:** Wrote a Verilog code with additional catalog IPs to create a countdown timer that can count down starting from an input given by the user.

## EXTRA-CURRICULAR ACTIVITIES

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- **Church service Deacon:** Partipate on weekly church services and help community memebbers in general during religious festivities
- **CCI community cup:** Help newly immigrants and other contestants in the registration process to take part in the competition